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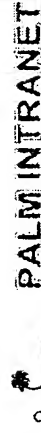
IEEE STD IEEE Standard

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- ☐ 1. Serial fault emulation  
Burgun, L.; Reblewski, F.; Fenelon, G.; Barbier, J.; Lepape, O.;  
[Design Automation Conference Proceedings 1996, 33rd](#)  
3-7 June 1996 Page(s):801 - 806  
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## Inventor Name Search Result

Your Search was:

Last Name = REBLEWSKI  
First Name = FREDERIC

Applicant#	Patent#	Status	Date Filed	Title	Inventor Name
08343319	5534333	150	10/13/1995	EMULATION SYSTEM HAVING A SCALABLE MULTI-LEVEL MULTI-STAGE PROGRAMMABLE INTERCONNECT NETWORK	REBLEWSKI, FREDERIC
08342838	5777489	150	10/13/1995	FIELD PROGRAMMABLE GATE ARRAY WITH INTEGRATED DEBUGGING FACILITIES	REBLEWSKI, FREDERIC
08352956	5754827	150	10/13/1995	METHOD AND APPARATUS FOR PERFORMING FULLY VISIBLE TRACING OF AN EMULATION	REBLEWSKI, FREDERIC
08362948	5790332	150	04/23/1996	METHOD AND APPARATUS FOR TRACING ANY NODE OF AN EMULATION	REBLEWSKI, FREDERIC
08363843	5801955	150	08/31/1996	METHOD AND APPARATUS FOR REMOVING TIMING HAZARDS IN A CIRCUIT DESIGN	REBLEWSKI, FREDERIC
08389322	5907692	150	07/20/1996	EMULATION SYSTEM HAVING A SCALABLE MULTI-LEVEL MULTI-STAGE HYBRID PROGRAMMABLE INTERCONNECT NETWORK	REBLEWSKI, FREDERIC
08380419	5831856	150	11/26/1997	METHOD AND APPARATUS FOR REMOVING TIMING HAZARDS IN A CIRCUIT DESIGN	REBLEWSKI, FREDERIC
08385322	6037206	150	12/04/1997	FIELD PROGRAMMABLE GATE ARRAY WITH INTEGRATED DEBUGGING FACILITIES	REBLEWSKI, FREDERIC
09062240	5929723	150	04/17/1998	METHOD AND APPARATUS TRACING ANY NODE OF AN EMULATION	REBLEWSKI, FREDERIC
09062326	Not Issued	161	04/17/1998	METHOD AND APPARATUS FOR PERFORMING FULLY VISIBLE TRACING OF AN EMULATION	REBLEWSKI, FREDERIC
09118311	Not Issued	161	07/16/1998	EMULATION SYSTEM HAVING A SCALABLE MULTI-LEVEL MULTI-STAGE HYBRID PROGRAMMABLE INTERCONNECT NETWORK	REBLEWSKI, FREDERIC
09404920	6247883	150	09/24/1999	REGIONALLY TIME MULTIPLEXED EMULATION SYSTEM	REBLEWSKI, FREDERIC
09404923	6234674	150	09/24/1999	CLOCK GENERATION AND DISTRIBUTION IN AN EMULATION SYSTEM	REBLEWSKI, FREDERIC
09404923	6263592	150	09/24/1999	RECONFIGURABLE INTEGRATED CIRCUIT WITH INTEGRATED DEBUGGING FACILITIES FOR USE IN AN EMULATION SYSTEM	REBLEWSKI, FREDERIC
09405602	6647362	150	09/24/1999	EMULATION SYSTEM SCALING	REBLEWSKI, FREDERIC
09405632	6473726	150	09/24/1999	METHOD AND APPARATUS FOR CONCURRENT EMULATION OF MULTIPLE CIRCUIT DESIGNS ON AN EMULATION SYSTEM	REBLEWSKI, FREDERIC
09424621	Not Issued	161	10/22/1999	EMULATION SYSTEM HAVING AN EFFICIENT EMULATION SIGNAL ROUTING	REBLEWSKI, FREDERIC

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## Inventor Name Search Result

Applicant#	Patent#	Status	Date Filed	Title	Inventor Name
09525210	6338463	150	03/14/2000	A RECONFIGURABLE INTEGRATED CIRCUIT WITH INTEGRATED DEBUGGING FACILITIES AND SCALABLE PROGRAMMABLE INTERCONNECT	REBLEWSKI, FREDERIC
09571349	6594810	150	10/04/2001	RECONFIGURABLE INTEGRATED CIRCUIT WITH A SCALABLE ARCHITECTURE	REBLEWSKI, FREDERIC
10001020	Not Issued	41	10/30/2001	Emulation components and system including distributed trace data collection and pre-processing	REBLEWSKI, FREDERIC
10003184	Not Issued	93	10/30/2001	EMULATION COMPONENTS AND SYSTEM INCLUDING DISTRIBUTED EVENT MONITORING, AND TESTING OF AN IC DESIGN UNDER EMULATION	REBLEWSKI, FREDERIC
10003951	7035782	150	10/30/2001	EMULATION COMPONENTS AND SYSTEM INCLUDING DISTRIBUTED ROUTING AND CONFIGURATION OF EMULATION RESOURCES	REBLEWSKI, FREDERIC
10043964	6834136	150	01/10/2002	CROSSBAR DEVICE WITH REDUCED PARASITIC CAPACITIVE LOADING AND USAGE OF CROSSBAR DEVICES IN RECONFIGURABLE CIRCUITS	REBLEWSKI, FREDERIC
10086813	6217433	150	02/28/2002	RECONFIGURABLE INTEGRATED CIRCUIT WITH INTEGRATED DEBUGGING FACILITIES AND SCALABLE PROGRAMMABLE INTERCONNECT	REBLEWSKI, FREDERIC
10223700	6839562	150	10/18/2002	METHOD AND APPARATUS FOR CONCURRENT EMULATION OF MULTIPLE CIRCUIT DESIGNS ON AN EMULATION SYSTEM	REBLEWSKI, FREDERIC
10458126	Not Issued	40	06/10/2003	Emulation of circuits with in-circuit memory	REBLEWSKI, FREDERIC
10602020	Not Issued	71	06/24/2003	Data compaction and pin assignment	REBLEWSKI, FREDERIC
10668236	Not Issued	93	09/24/2003	REGIONALLY TIME MULTIPLEXED EMULATION SYSTEM	REBLEWSKI, FREDERIC
10673665	Not Issued	30	09/20/2003	Message-based low latency circuit emulation signal transfer	REBLEWSKI, FREDERIC
10736908	Not Issued	20	12/17/2003	Distributed configuration of integrated circuits in an emulation system	REBLEWSKI, FREDERIC
10806235	Not Issued	161	03/23/2004	Reconfigurable integrated circuit with integrated debugging facilities and scalable programmable interconnect	REBLEWSKI, FREDERIC
10824482	Not Issued	30	04/15/2004	Logic design modeling and interconnection	REBLEWSKI, FREDERIC
11181036	Not Issued	30	07/12/2005	Software state replay	REBLEWSKI, FREDERIC
11225740	Not Issued	25	09/13/2005	Reconfigurable circuit with redundant reconfigurable cluster(s)	REBLEWSKI, FREDERIC
11268932	Not Issued	30	11/08/2005	Configurable circuit with configuration data protection features	REBLEWSKI, FREDERIC
11268933	Not Issued	25	11/08/2005	Reconfigurable system with corruption detection and recovery	REBLEWSKI, FREDERIC
11285983	Not Issued	30	11/23/2005	Runtime reconfiguration of reconfigurable circuits	REBLEWSKI, FREDERIC
11311718	Not Issued	30	12/19/2005	Configurable circuits with microcontrollers	REBLEWSKI, FREDERIC
11330418	Not Issued	20	01/10/2006	On circuit finalization of configuration data in a	REBLEWSKI, FREDERIC

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Issued			reconfigurable circuit	
11358338	Not Issued	30	02/21/2006	Packet-oriented communication in reconfigurable circuit(s)
60387217	Not Issued	159	07/12/2004	Software state replay
				REBLEWSKI, FREDERIC
				REBLEWSKI, FREDERIC

Inventor Search Completed: No Records to Display.

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L1	18	(US-20020095649-\$ or US-20020162084-\$ or US-20030131335-\$ or US-20010049816-\$).did. or (US-6996504-\$ or US-6321181-\$ or US-6345240-\$ or US-6717433-\$ or US-6265894-\$ or US-6388465-\$ or US-5574388-\$ or US-5852740-\$ or US-5907697-\$ or US-6816826-\$ or US-6594810-\$ or US-5754827-\$ or US-5796623-\$ or US-6609243-\$). did.	US-PGPUB; USPAT	OR	OFF	2006/07/12 10:06
L2	33	REBLEWSKI.in.	US-PGPUB; USPAT	OR	OFF	2006/07/12 10:16
L3	33	L2 and (first second third)	US-PGPUB; USPAT	OR	OFF	2006/07/12 10:17
L4	31	L2 and (first second third).clm.	US-PGPUB; USPAT	OR	OFF	2006/07/12 10:17
L5	12	L2 and (first and second and third). clm.	US-PGPUB; USPAT	OR	OFF	2006/07/12 10:17
L6	11	L2 and (first with second with third). clm.	US-PGPUB; USPAT	OR	OFF	2006/07/12 10:19
L7	11	L2 and (first same second same third).clm.	US-PGPUB; USPAT	OR	OFF	2006/07/12 10:19
L8	4	(US-5907697-\$ or US-6594810-\$ or US-6647362-\$ or US-6947882-\$). did.	USPAT	OR	OFF	2006/07/12 10:22
L9	272	710/317.ccls.	US-PGPUB; USPAT	OR	OFF	2006/07/12 10:38
L10	194	L9 and (interconnect XBAR crossbar)	US-PGPUB; USPAT	OR	OFF	2006/07/12 10:41
L11	2	("4306286" "6141636").pn.	US-PGPUB; USPAT	OR	OFF	2006/07/12 10:41

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	18	(US-20020095649-\$ or US-20020162084-\$ or US-20030131335-\$ or US-20010049816-\$).did. or (US-6996504-\$ or US-6321181-\$ or US-6345240-\$ or US-6717433-\$ or US-6265894-\$ or US-6388465-\$ or US-5574388-\$ or US-5852740-\$ or US-5907697-\$ or US-6816826-\$ or US-6594810-\$ or US-5754827-\$ or US-5796623-\$ or US-6609243-\$). did.	US-PGPUB; USPAT	OR	OFF	2006/07/12 10:03
S1	2	(simulat\$4 adj processor) with (interconnect crossbar)	USPAT	OR	OFF	2006/07/10 11:15
S2	9	(simulat\$4 with processor) with (interconnect crossbar)	USPAT	OR	OFF	2006/07/10 11:15
S3	443	(process\$4 with element) with (interconnect crossbar)	USPAT	OR	OFF	2006/07/10 11:16
S4	28	(simulat\$4 with processor) with (interconnect crossbar)	US-PGPUB; USPAT	OR	OFF	2006/07/10 11:15
S5	155	(process\$4 adj element) with (interconnect crossbar)	USPAT	OR	OFF	2006/07/10 11:17
S6	234	(process\$4 adj element) with (interconnect crossbar)	US-PGPUB; USPAT	OR	OFF	2006/07/10 11:24
S7	1925	multilevel with (interconnect crossbar)	US-PGPUB; USPAT	OR	OFF	2006/07/10 11:24
S8	5713	program\$8 with (interconnect crossbar)	US-PGPUB; USPAT	OR	OFF	2006/07/10 11:24
S9	22	S7 and S8	US-PGPUB; USPAT	OR	OFF	2006/07/11 09:31
S10	835	(distribut\$6 with simulat\$4) and (crossbard interconnect (inter adj connect))	US-PGPUB; USPAT	OR	OFF	2006/07/11 09:31
S11	71	(distribut\$6 adj simulat\$4) and (crossbard interconnect (inter adj connect))	US-PGPUB; USPAT	OR	OFF	2006/07/11 09:49
S12	9799	(multilevel (multi adj level)) and (crossbard interconnect (inter adj connect))	US-PGPUB; USPAT	OR	OFF	2006/07/11 09:50
S13	2936	(multilevel (multi adj level)) with (crossbard interconnect (inter adj connect))	US-PGPUB; USPAT	OR	OFF	2006/07/11 09:50
S14	286	S13 and (simulat\$4 emulat\$4)	US-PGPUB; USPAT	OR	OFF	2006/07/11 10:42

## EAST Search History

S15	272	710/317.ccls.	US-PGPUB; USPAT	OR	OFF	2006/07/11 09:51
S16	608	716/16.ccls.	US-PGPUB; USPAT	OR	OFF	2006/07/11 10:50
S17	657	716/17.ccls.	US-PGPUB; USPAT	OR	OFF	2006/07/11 10:43
S18	219	S16 and S17	US-PGPUB; USPAT	OR	OFF	2006/07/11 11:02
S19	1	"10/824489"	US-PGPUB; USPAT	OR	OFF	2006/07/11 11:02
S20	608	716/16.ccls.	US-PGPUB; USPAT	OR	OFF	2006/07/11 18:40
S21	370	716/16.ccls. and (interconnect XBAR crossbar)	US-PGPUB; USPAT	OR	OFF	2006/07/11 18:41